

# Slater & Matsil, L.L.P.

**Suite 1000** 17950 Preston Road Dallas, Texas 75252-5793

Phone: 972-732-1001 Facsimile: 972-732-9218

#### **FACSIMILE COVER SHEET**

To Office of Initial Patent Examination

Total Pages Sent:

11

**ATTN: Missing Parts** 

(including cover sheet)

Facsimile Number: 703-746-4060

Transmission Date:

August 11, 2004

#### IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Schloesser, et al.

Docket No.:

INF-135

Serial No:

10/777,128

Art Unit:

2818

Date Filed:

February 13, 2004

TITLE:

Method of Fabricating and Architecture for Vertical Transistor Cells and

Transistor-Controlled Memory Cells

#### CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the U.S. Patent and Trademark Office at 703-746-4060 on the date shown above:

- Certification of Facsimile Transmission (1 page)
- Associate Power of Attorney (1 page)
- Change of Correspondence Address (1 page)
- Response to Notice to File Missing Parts (in duplicate = 2 pages)
- Copy of Notice to File Missing Parts of Nonprovisional Application (2 pages)
- Fully executed Declaration for Patent Application... (4 pages)

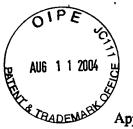
Respectfully submitted,

Natalie Surider

Natalie Swider Legal Assistant

Confirmation Respectfully Requested

This facsimile is intended only for the use of the address named and contains legally privileged and/or confidential information. If you are not the intended recipient of this telecopy, you are hereby notified that any dissemination, distribution, copying or use of this communication is strictly prohibited. Applicable privileges are not waived by virtue of the document having been transmitted by Facsimile. Any misdirected facsimiles should be returned to the sender by mail at the address indicated on this cover sheet.



## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Schloesser, et al.

Docket No.:

INF-135

Serial No.:

10/777,128

Art Unit:

2818

Filed:

February 13, 2004

Examiner:

TBD

For:

Method of Fabricating and Architecture for Vertical Transistor Cells and

Transistor-Controlled Memory Cells

Mail Stop: Missing Parts Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

# Response to Notice to File Missing Parts of Nonprovisional Application

Dear Sir:

In response to the Notice to File Missing Parts of Nonprovisional Application mailed May 11, 2004 (copy enclosed), Applicants respectfully submit the Declaration for Patent Application, Power of Attorney & Designation of Correspondence Address signed and dated by all three inventors.

Please charge the required \$130.00 late oath or declaration surcharge and the \$110.00 extension for response within first month surcharge to Deposit Account No. 50-1065. Please charge any additional fees or credit any overpayments to the same.

8/11/04

Date

Respectfully submitted,

Ira S. Matsil

Reg. No. 35,272

Attorney for Applicants

Slater & Matsil, L.L.P. 17950 Preston Rd., Suite 1000 Dallas, TX 75252

Tel: 972-732-1001 Fax: 972-732-9218

08/12/2004 JBALINAN 00000089 501065 10777128

02 FC:1251

110.00 DA

9727329218



### United States Patent and Trademark Office

UNITED STATES DEPARTMENT OF COMMERCE United States Patent and Trademark Office

APPLICATION NUMBER

FILING OR 371 (c) DATE

FIRST NAMED APPLICANT

ATTORNEY DOCKET NUMBER

10/777,128

02/13/2004

Till Schloesser

INF-135

SHAW PITTMAN LLP 1650 Tysons Boulevard McLean, VA 22102-4859

**CONFIRMATION NO. 3801 FORMALITIES LETTER** OC000000012590174\*

Date Mailed: 05/11/2004



### NOTICE TO FILE MISSING PARTS OF NONPROVISIONAL APPLICATION

FILED UNDER 37 CFR 1.53(b)

Filing Date Granted

### Items Required To Avoid Abandonment:

An application number and filing date have been accorded to this application. The item(s) indicated below, however, are missing. Applicant is given TWO MONTHS from the date of this Notice within which to file all required items and pay any fees required below to avoid abandonment. Extensions of time may be obtained by filing a petition accompanied by the extension fee under the provisions of 37 CFR 1.136(a).

- The oath or declaration is unsigned.
- To avoid abandonment, a late filing fee or oath or declaration surcharge as set forth in 37 CFR 1.16(e) of \$130 for a non-small entity, must be submitted with the missing items identified in this letter.

### **SUMMARY OF FEES DUE:**

Total additional fee(s) required for this application is \$130 for a Large Entity

\$130 Late oath or declaration Surcharge.

Replies should be mailed to:

Mail Stop Missing Parts

Commissioner for Patents

P.O. Box 1450

Alexandria VA 22313-1450

A copy of this notice MUST be returned with the reply.

Center

PAGE 6/11 \* RCVD AT 8/11/2004 5:41:22 PM [Eastern Daylight Time] \* SVR:USPTO-EFXRF-2/0 \* DNIS:7464060 \* CSID:9727329218 \* DURATION (mm-ss):03-18

Page 2 of 2

Initial Patent Examination Division (703) 308-1202
PART 2 - COPY TO BE RETURNED WITH RESPONSE